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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2

Application Number:	09/540,238
Filing Date:	01 April 2000
First Named Inventor:	S. Chaganty
Art Unit:	2131
Examiner Name:	Leynna A. Ha
Attorney Docket No.:	CyberIQ M-8403

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
LH	AA	5,283,897	02/01/1994	Georgiadis, et al.	
LH	AB	5,301,226	04/1994	Olson, et al.	
LH	AC	5,473,599	12/5/1995	Li, et al.	
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Substitute for form 1449/PTO				Application Number:	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Filing Date:	
				First Named Inventor:	
				Art Unit:	
				Examiner Name:	
Sheet	2	of	2	Attorney Docket No.:	CyberIQ M-8403

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LH	BW	JP - 409321789	12/1997			
LH	BX	WO 99/32956	07/01/1999	Bhaskaran, et al.		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[6]
LH	BY	Internet -- "Quasi-Dynamic Load-Balancing (QDLB) Methods." 25 April 1995, pp. 2 and 5.	
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LH	CA	Internet -- Becker, Wolfgang, "Dynamic Load Balancing For Parallel Database Processing," Institute of Parallel and Distributed High-Performance Systems (IPVR), University of Stuttgart Breitwiesenstr, Stuttgart, Germany, 1997.	
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Examiner Signature		Date Considered	1/9/06